IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Stefano Sivero et al.

Serial No.: 10/666,142

Filed: September 17, 2003

For: REGENERATIVE CLOCK REPEATER

PATENT APPLICATION

Group Art Unit: 2631

Examiner:

Supplemental Information Disclosure Statement

Hon. Commissioner for Patents
Alexandria, VA 22313

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 CFR § 1.56. A copy of the reference is enclosed.

Other Reference

H.B. Bakoglu, *"Circuit, Interconnections, and Packaging for VLSI"*, Addison-Wesley Publishing Company, Inc., 1990, pages 198-219.

Respectfully submitted,

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ATM36:249.IDS

FORM PTO-1449			Atty. Docket No. ATM-249		Serial No. 10/666,142		
LIST OF PRIOR ART CITED BY APPLICANT			Applicant: Stefano Sivero et al.				
			Filing Date: Sept. 17, 2003		Group: 2631		

U.S. PATENT DOCUMENTS

Examiner Initial*		Document Number	Grant Date	Name	Class	Sub Class	Filing Date
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

Examiner Initial*		Document Number	Grant Date	Country	Class	Sub Class	Translation Yes No
	AK						
	AL						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AM	H.B. Bakoglu, "Circuit, Interconnections, and Packaging for VLSI", Addison-Wesley Publishing Company, Inc., 1990, pages 198-219.
	AN	
	AO	

EXAMINER:	DATE CONSIDERED:

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.